

Notice of References Cited

Application/Control No.

09/682,611

Applicant(s)/Patent Under
Reexamination
KANZAKI ET AL.

Examiner

Donna K. Mason

Art Unit

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Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,553,445	04-2003	Drapkin et al.	710/305
	B	US-5,931,927	08-1999	Takashima, Daisaburo	710/65
	C	US-5,890,005	03-1999	Lindholm, Rune	713/320
	D	US-6,584,572	06-2003	Choi, Jung-hwan	713/320
	E	US-6,243,779	06-2001	Devanney et al.	710/305
	F	US-6,208,177	03-2001	Knoedl, Jr., George	327/108
	G	US-4,667,337	05-1987	Fletcher, Roderick J.	377/41
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hong, Sungpack, et al., "Bus-Invert Coding for Low Power I/O - A Decomposition Approach," Aug. 8-11, 2000, Proceedings of the 43rd IEEE Midwest Symposium on Circuits and Systems, Vol. 2, p. 750-752.
	V	Stan, Mircea R., et al., "Bus-Invert Coding for Low-Power I/O," March 3, 1995, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 3, Issue 1, p. 49-58.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.